

REMARKS

Claims 1-2 are pending in the application. Claim 2 has been amended in form and for clarity. The Applicant reserves the right to pursue the original claims in this and other applications. Applicant gratefully acknowledges the allowance of claim 2.

Replacement FIGs. 6-8 are being submitted herewith. A legend designating "Prior Art" has been added to the figures. Applicant respectfully requests withdrawal of the objection.

Claim 1 is rejected under 35 U.S.C. §102 as being anticipated by U.S. Patent 6,211,728 to Chen et al. ("Chen"). Reconsideration is respectfully requested.

The present invention relates to a pulse width modulation amplifier which is capable of suppressing an output distortion. The present invention has two direct-current power sources, such as Vcc1, Vcc2 shown in FIG. 5, for example. A first series circuit including a first and second switching element is connected to a positive side of one of the direct-current power sources, and a second series circuit, including third and fourth switching elements, is connected to a positive pole of a second direct-current power source. Further, one end of a load circuit is connected to a connecting point between the first and second switching elements, and another end of the load circuit is connected to a connecting point between the third and fourth switching elements.

In this circuit arrangement, the driving circuit turns on the set of switching elements including the first and the fourth switching elements, and the set of switching elements including the second and the third switching elements. The sets are alternatively switched to the ON state. Accordingly, as described on page 18 of the specification, the power is supplied alternately from the first direct-current power source and the second direct-current power source.

Therefore, as described on page 16 of the specification of the present invention, even if the output voltage of one of the direct-current power sources is lowered, the output voltage can be restored while the other direct-current power source is supplying the power source current.

Chen discloses a bridge circuit have a single direct-current power source (see FIG. 1). Chen does not disclose or suggest implementing two direct-current sources which are alternately activated. Accordingly, as Chen does not disclose every limitation of claim 1, Applicant respectfully requests withdrawal of the rejection.

In view of the above amendment, applicant believes the pending application is in condition for allowance.

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Respectfully submitted,

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AMENDMENTS TO THE DRAWINGS

Replacement FIGs. 6-8 are being submitted herewith. A legend designating the figures "Prior Art" has been added.